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problems: (a)

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1.20: Convert decimal +49 and +29 to binary, using the signed-2's-complement representation and

e Q. 1.19: The following decimal numbers are shown in sign-magnitude form: +9,286 and +801

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following division in

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1.6: The solutions  
to the quadratic  
equation  $x^2 - 11x + 22 = 0$  are  $x = 3$   
and  $x = 6$ .

---

Digital Design: Q.

1.10: Convert the  
following binary  
numbers to

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decimal: (a), (b)

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~~Decimal, Binary,~~

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(Part-1) - Digital

and logic design |

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Q. 3.31: Write a Verilog gate-level description of the circuit shown in (a) Fig. 3.22 (a)

---

Q. 2.14: Implement the Boolean function  $F = xy + x'y' + y'z$  (a) With AND, OR, and inverter gates

~~Getting Started in Digital Design~~  
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1.9: Express the following numbers in decimal: (a)

$(10110.0101)_2$  (b)

$(16.5)_{16}$

---

Q. 4.11: Using four half-adders

(HDL—see Problem 4.54), (a) Design a full-subtractor

circuit increment

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Coefficient  $a_{-1} = 0$

$a_{-2} = 1$   $a_{-3} = 0$   $a_{-4}$

$= 1$

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$$1.6. (x - 3)(x - 6)$$

$$= x^2 - (6 + 3)x +$$

$$6 \cdot 3 = x^2 - 11x + 22$$

Therefore:  $6 + 3 =$

$b + 1m$ , so  $b = 8$

Also,  $6 \cdot 3 = (18)_{10}$

$= (22)_8$ . 1.7.

$64CD_{16} = 0110\_01$

$00\_1100\_1101_2 =$

$110\_010\_011\_001$

$\_101 = (62315 \dots)$

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Description. For sophomore courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. Digital Design, fifth edition is a modern update

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